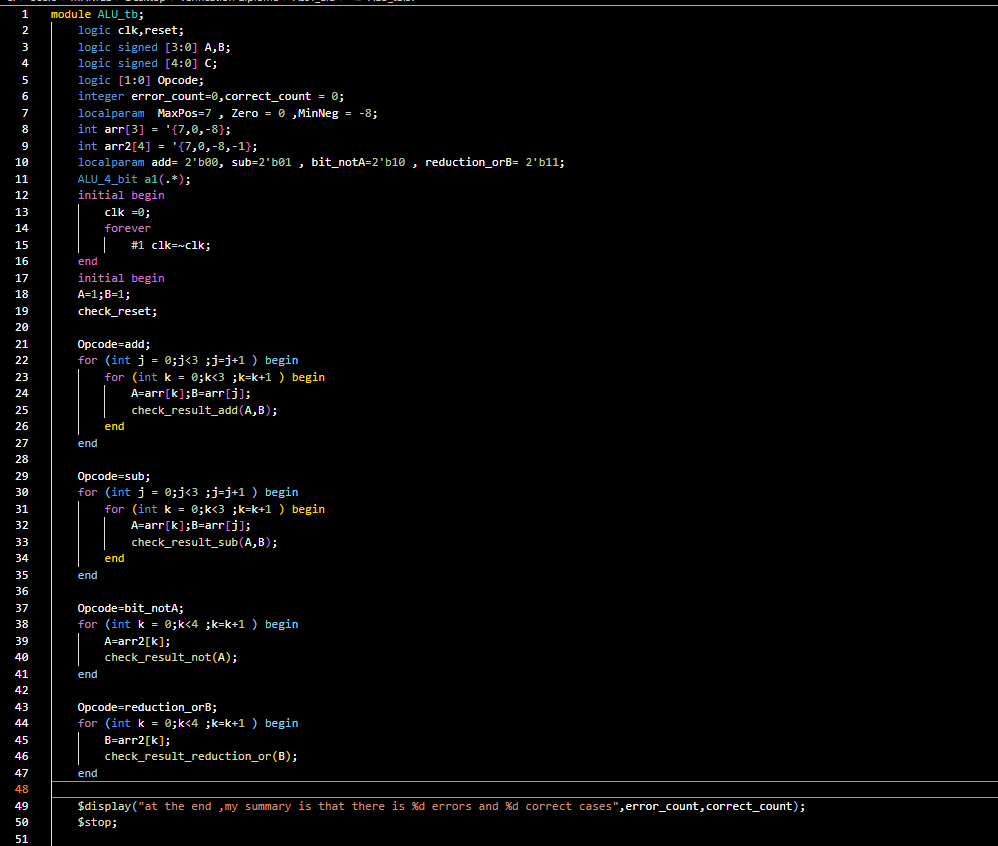
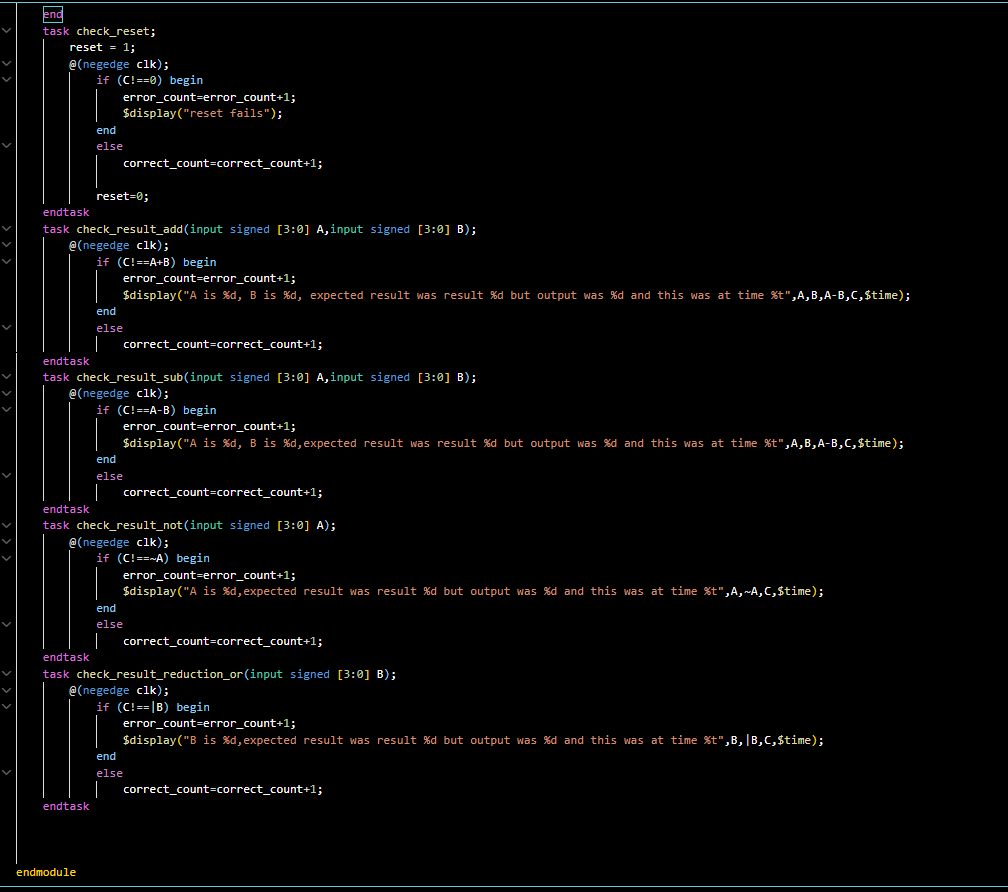
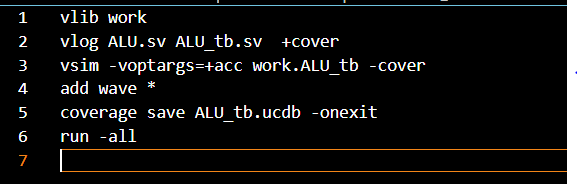
ALU\_Assignment1

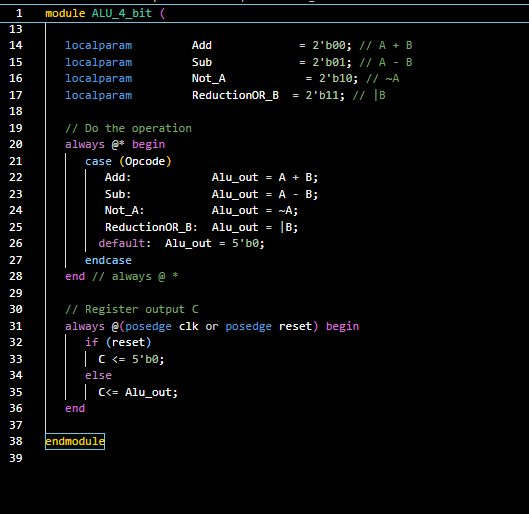
Testbench Captures:



Run.do File:

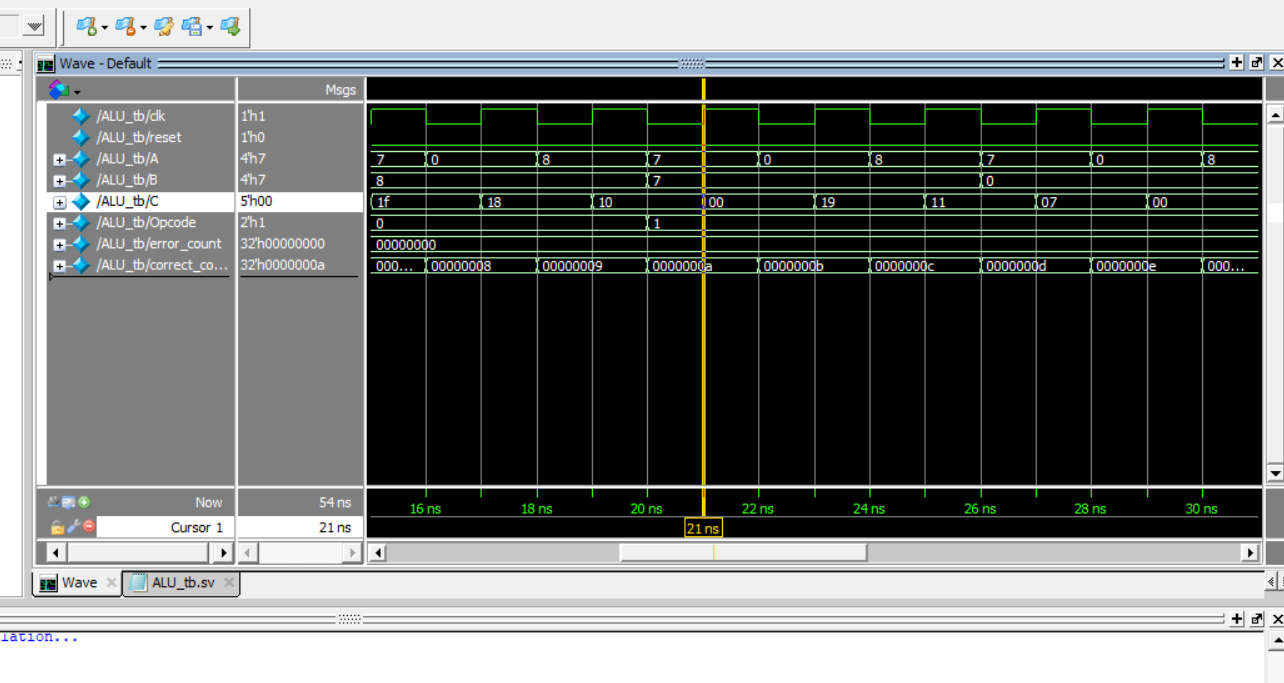


Design Snippet (not changed):

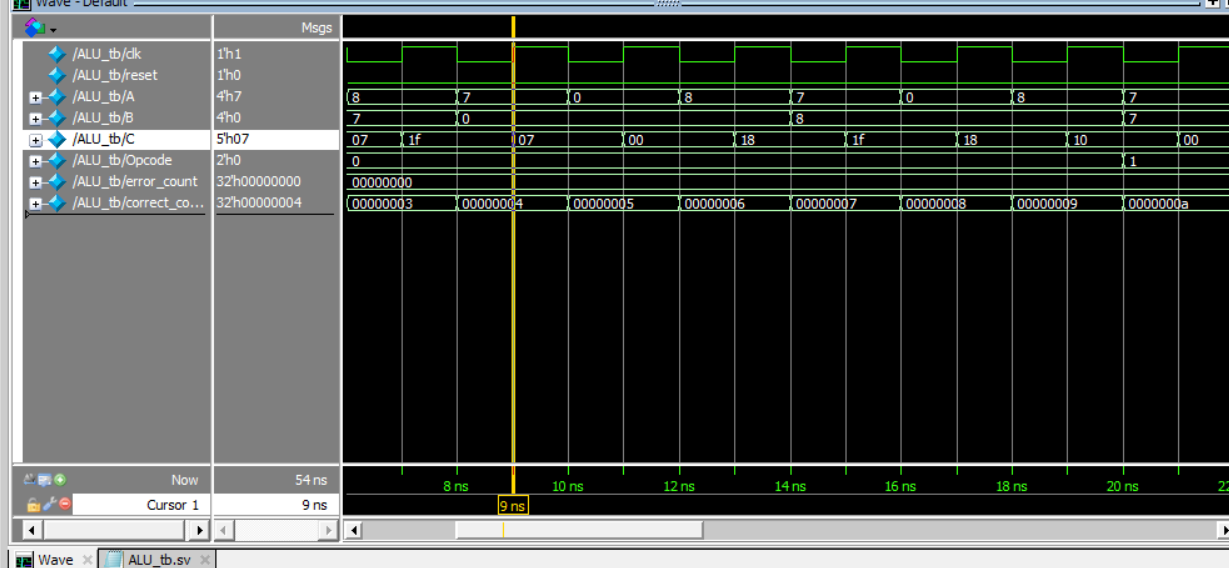


Questa Waveforms:

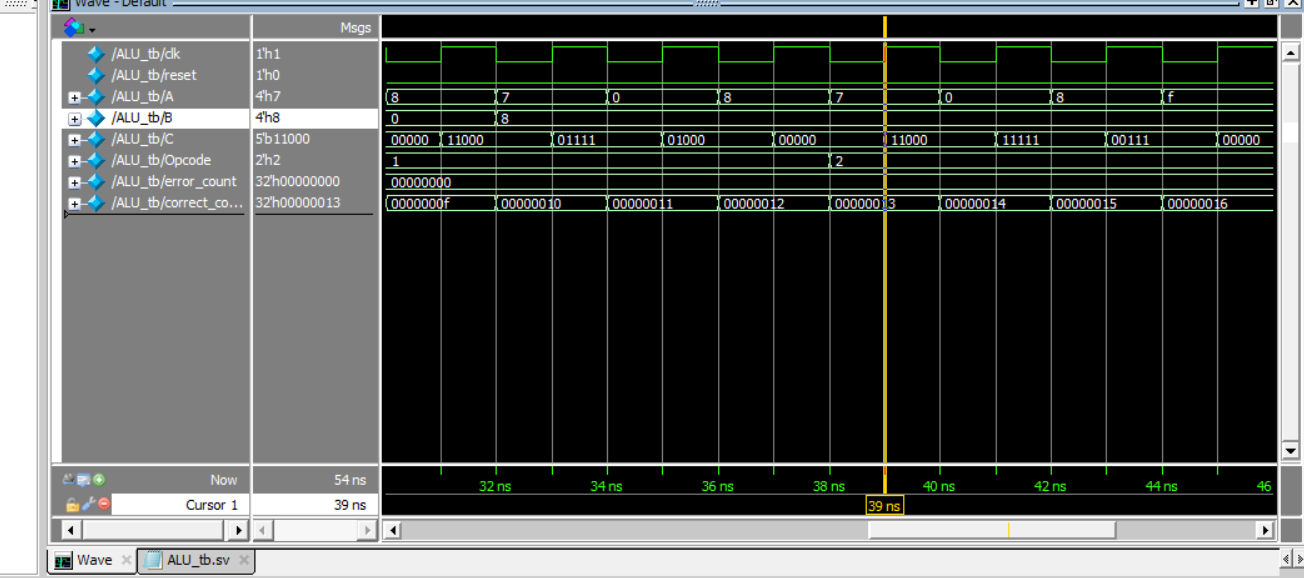
Subtract



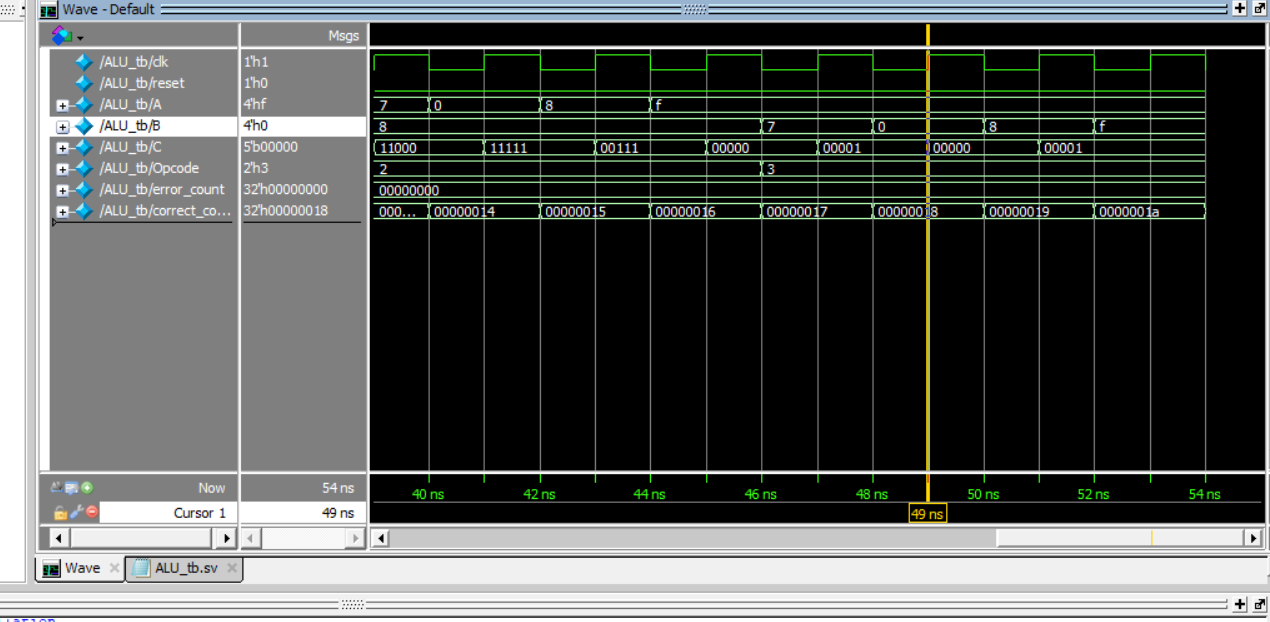
Add



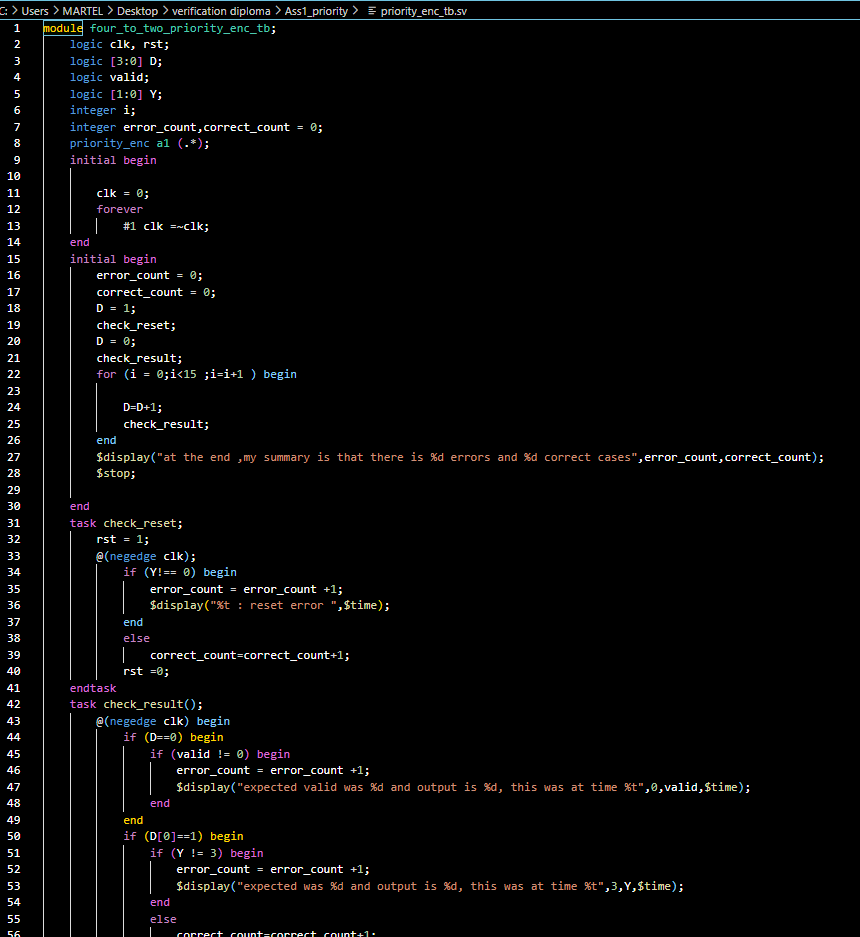
Bitwise Not

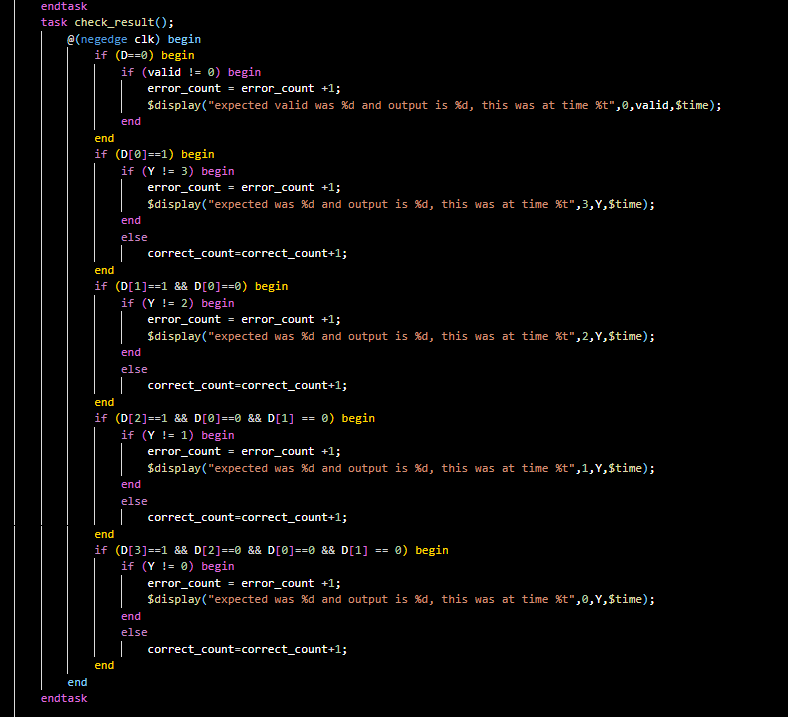


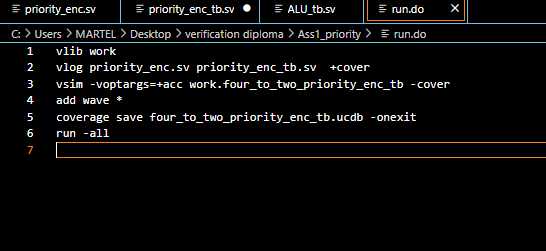
Reduction or



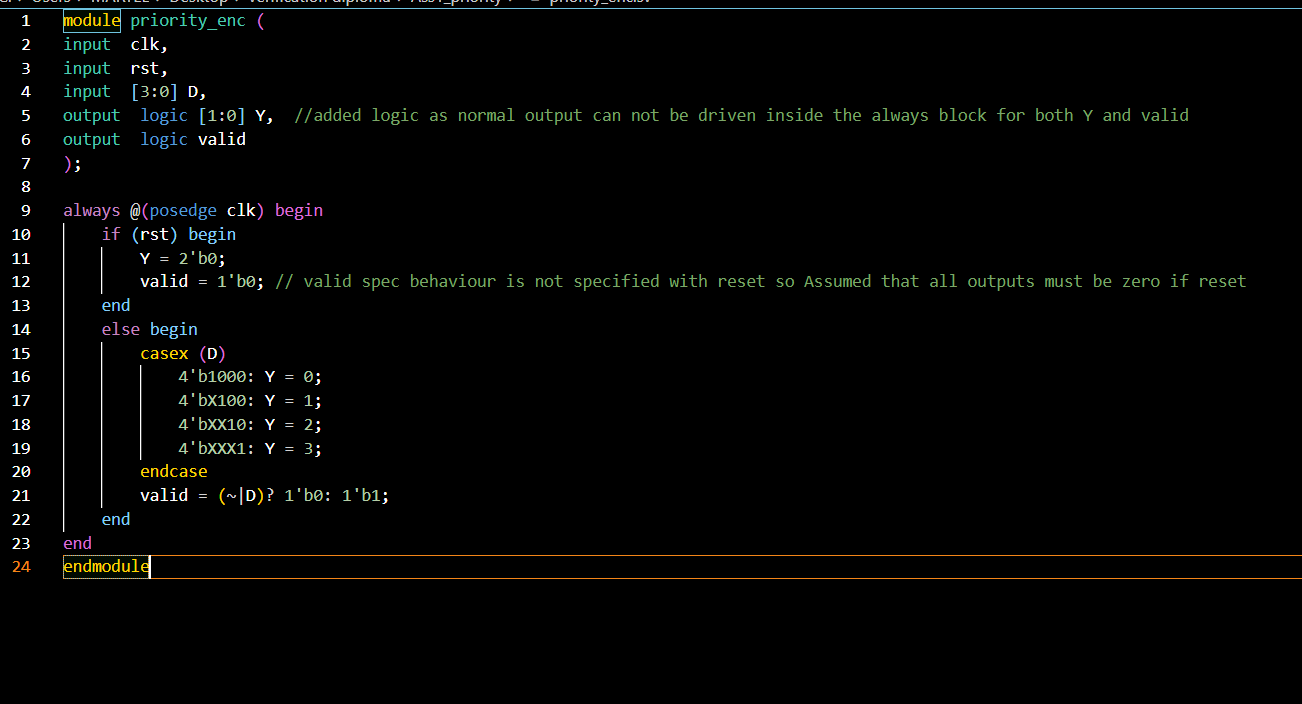
Priority Encoder Snippets

Testbench:



Run.do File:

Design:



Waveforms:

